

## SEMESTER S3

### SOLID STATE DEVICES

<b>Course Code</b>	<b>PCECT302</b>	<b>CIE Marks</b>	40
<b>Teaching Hours/Week (L: T:P: R)</b>	3:1:0:0	<b>ESE Marks</b>	60
<b>Credits</b>	4	<b>Exam Hours</b>	2 Hrs. 30 Min.
<b>Prerequisites (if any)</b>	Physics of Electrical Science (GBPHT121)	<b>Course Type</b>	Theory

#### Course Objectives:

1. This course explains the physical processes and working principles of semiconductor devices, while relating the device performance to material parameters and design criteria.

### SYLLABUS

<b>Module No.</b>	<b>Syllabus Description</b>	<b>Contact Hours</b>
1	<p><b>Review of Semiconductor physics:</b> Equilibrium and steady state conditions, Concept of effective mass and Fermi level, Density of states &amp; Effective density of states, Equilibrium concentration of electrons and holes.</p> <p><b>Excess carriers in semiconductors:</b> Generation and recombination mechanisms of excess carriers, quasi-Fermi levels.</p> <p><b>Carrier transport in semiconductors:</b> Drift, conductivity and mobility, variation of mobility with temperature and doping, Hall Effect. Diffusion, Einstein relations, Poisson equations, Continuity equations, Current flow equations, Diffusion length, Gradient of quasi-Fermi level.</p>	13
2	<p><b>PN junctions:</b> Contact potential, Electrical Field, Potential and Charge distribution at the junction, Biasing and Energy band diagrams, Ideal diode equation.</p> <p><b>Bipolar junction transistor:</b> Transistor action, Base width</p>	12

	modulation, Current components in a BJT, Derivation of current components.	
3	<p><b>Metal Semiconductor contacts:</b> Electron affinity and work function, Ohmic and Rectifying Contacts, current voltage characteristics.</p> <p><b>Ideal MOS capacitor:</b> band diagrams at equilibrium, accumulation, depletion and inversion, surface potential, CV characteristics, effects of real surfaces, threshold voltage, body effect.</p> <p><b>MOSFET-</b> Drain current equation of enhancement type MOSFET (derivation)- linear and saturation region, Drain characteristics, transfer characteristics.</p>	11
4	<p><b>MOSFET scaling:</b> Need for scaling, constant voltage scaling and constant field scaling. Sub- threshold conduction in MOS.</p> <p><b>Short channel effects in MOSFETs:</b> Channel length modulation, Drain Induced Barrier Lowering, Velocity Saturation, Threshold Voltage Variations and Hot Carrier Effects.</p> <p><b>MESFET and FinFET:</b> Structure, operation and advantages.</p>	8

**Course Assessment Method  
(CIE: 40 marks, ESE: 60 marks)**

**Continuous Internal Evaluation Marks (CIE):**

Attendance	Assignment/ Microproject	Internal Examination-1 (Written)	Internal Examination- 2 (Written )	Total
5	15	10	10	40

### End Semester Examination Marks (ESE)

*In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions*

Part A	Part B	Total
<ul style="list-style-type: none"> <li>• 2 Questions from each module.</li> <li>• Total of 8 Questions, each carrying 3 marks</li> </ul> <p><b>(8x3 =24marks)</b></p>	<ul style="list-style-type: none"> <li>• Each question carries 9 marks.</li> <li>• Two questions will be given from each module, out of which 1 question should be answered.</li> <li>• Each question can have a maximum of 3 sub divisions.</li> </ul> <p><b>(4x9 = 36 marks)</b></p>	<b>60</b>

### Course Outcomes (COs)

At the end of the course students should be able to:

Course Outcome		Bloom's Knowledge Level (KL)
<b>CO1</b>	Apply Fermi-Dirac statistics to compare equilibrium carrier concentration.	<b>K3</b>
<b>CO2</b>	State different carrier transport mechanisms in extrinsic semiconductors and obtain the current densities due to this transport.	<b>K3</b>
<b>CO3</b>	Apply the concept of semiconductor physics to solve the current components in semiconductor devices.	<b>K3</b>
<b>CO4</b>	Analyze the response of semiconductor devices for different biasing conditions	<b>K3</b>
<b>CO5</b>	Outline the effects of scaling in semiconductor devices.	<b>K2</b>

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

### CO-PO Mapping Table (Mapping of Course Outcomes to Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
<b>CO1</b>	3											
<b>CO2</b>	3	2										
<b>CO3</b>	3	2										2
<b>CO4</b>	3	2	2									2
<b>CO5</b>	3	2	2									2

Note: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

<b>Text Books</b>				
<b>Sl. No</b>	<b>Title of the Book</b>	<b>Name of the Author/s</b>	<b>Name of the Publisher</b>	<b>Edition and Year</b>
1	Semiconductor Physics and Devices	Neamen	McGraw Hill	4/e, 2017
2	Physics of Semiconductor Devices	Sze S.M	John Wiley	3/e, 2015
3	Semiconductor Devices: Physics and Technology	Sze S.M	John Wiley	3/e, 2016
4	Operation and Modelling of the MOS Transistor	Yannis Tsividis	Oxford University Press	3/e, 2010

<b>Video Links (NPTEL, SWAYAM...)</b>	
<b>Module No.</b>	<b>Link ID</b>
1	<a href="https://nptel.ac.in/courses/117106091">https://nptel.ac.in/courses/117106091</a>
2	<a href="https://nptel.ac.in/courses/117106091">https://nptel.ac.in/courses/117106091</a>
3	<a href="https://nptel.ac.in/courses/117106091">https://nptel.ac.in/courses/117106091</a>
4	<a href="https://nptel.ac.in/courses/117106091">https://nptel.ac.in/courses/117106091</a>